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| APPLICATION | FILING or   | GRP ART |               |                           |            |            |
|-------------|-------------|---------|---------------|---------------------------|------------|------------|
| NUMBER      | 371(c) DATE | UNIT    | FIL FEE REC'D | ATTY.DOCKET.NO            | TOT CLAIMS | IND CLAIMS |
| 10/588,984  | 11/21/2006  | 2836    | 1080          | 14603-026US1 P2004,0133 U | 21         | 2          |

**CONFIRMATION NO. 4272** 

**FILING RECEIPT** 

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Date Mailed: 08/12/2008

26161 FISH & RICHARDSON PC P.O. BOX 1022 MINNEAPOLIS, MN 55440-1022

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## Applicant(s)

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## **Power of Attorney:**

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#### Domestic Priority data as claimed by applicant

This application is a 371 of PCT/EP05/01476 02/14/2005

## **Foreign Applications**

GERMANY 10 2004 007 241.8 02/13/2004 GERMANY 10 2004 056 222.9 11/22/2004

## If Required, Foreign Filing License Granted: 08/08/2008

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is **US 10/588,984** 

**Projected Publication Date: 11/20/2008** 

Non-Publication Request: No

Early Publication Request: No

#### Title

Circuit Arrangement and Method For Protecting an Integrated Semiconductor Circuit

## **Preliminary Class**

361

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